Annexure - II

Advertisement for the post of JRF in SERB CRG (Now ANRF) funded Research Project

Applications are invited from Indian nationals for the position of JRF in a SERB-CRG (Now ANRF) research project with the following details:

Title of the Project: A Systematic Design of Robust Memory Cell Using Ferroelectric FET (FeFET) for AI Hardware Accelerator

Principal Investigator: Dr. Nitanshu Chauhan, Assistant Professor, Electronics and Communication Engineering, IIT Bhilai, Bhilai, Chhattisgarh- 491002 (nitanshuc@iitbhilai.ac.in). For other details and the project team, please visit the homepage- https://www.iitbhilai.ac.in/index.php?pid=nitanshuc

Name of the position: JRF

Number of Positions: 1 (one)

Essential qualifications:

Minimum Essential Qualifications:
 ● **B.E./B.Tech. in ECE/allied branches with a minimum 60%-mark (or a minimum CGPA of 6.5 on a 10-point scale) + GATE/UGC-NET/CSIR NET or equivalent examination qualification and minimum two (02) years relevant VLSI Industry/research experience**
OR
● **M.E./M.Tech. in ECE/allied branches with a minimum 60% mark (or a minimum CGPA of 6.5 on a 10-point scale) + GATE/UGC-NET/CSIR NET or equivalent examination qualification**

Desirable: Candidate should have knowledge in basic semiconductor, solid-state devices, MOS Device Physics and Modelling, CMOS-based circuit design, and Memory Design.
Desirable:
   • EDA/TCAD tools, Cadence tools, TCAD device/process simulation, mixed-mode simulation
   • Hardware Descriptive Language: Verilog or Verilog-A (Desirable)

Age limit: Should not exceed 28 years on the last date of submission of the online application. Age relaxation may be given to outstanding candidates or candidates with relevant experience.

Salary:
   (i)   **Rs. 37,000/- (Rupees thirty-seven thousand only) plus 18% HRA per month for the scholars who are selected through**
     (a) **National Eligibility Tests-CSIR-UGC NET including Lectureship (Assistant Professorship) or GATE or**

(b) **A selection process through National Level Examinations conducted by Central Government Departments and their Agencies, and Institutions.**

Duration: 1 year, extended for another year based on performance.

How to apply: Interested candidates must apply on the application form attached with this advertisement. The duly completed application form, along with the candidate's resume, must be mailed or emailed to the PI to reach by 22/10/2025. Only shortlisted candidates will be called for interview. The positions are available immediately.

Due date: The application must reach the PI, (nitanshuc@iitbhilai.ac.in), by 22/10/2025

Terms & Conditions:

  i.   No TA/DA will be provided to the candidate for the interview

  ii.   The PI shall not be responsible for postal/courier delay if any, or any other reason for non-receipt of the document at the specified time and will result in disqualification/rejection of the application.

  iii. The decision of the selection committee will be final.

  iv.   If the number of shortlisted candidates called for the interview is large, the selection committee may decide to restrict the number of candidates for the interview to a reasonable limit after considering qualifications and experience over and above the minimum prescribed in the advertisement.

  v.   The appointment of the candidate will be governed by the terms and conditions of the Institute/ funding agency particularly applicable to the said project as and when required.

  vi.   The selected candidate will have to join duty immediately on receipt of the offer.

  vii. The fellowship may be terminated with a 30-day notice before completion of the tenure if performance till date is not deemed satisfactory.